

BRIEF DESCRIPTION OF THE DRAWINGS

The drawings referenced herein form a part of the specification. Features shown in the drawing are meant as illustrative of only some embodiments of the invention, and not of all embodiments of the invention, unless otherwise explicitly indicated, and implications to the contrary are otherwise not to be made.

Figure 1 is a high-level flow chart of a method in accordance with an embodiment of the present invention.

Figure 2 shows an SDT junction in accordance with an embodiment of the present invention.

Figure 3 shows the coercivity (H_c) of the SDT junction at room temperature.

Figure 4 shows the coercivity (H_c) of the SDT junction at 50° C above room temperature.

Figure 5 is a flow chart illustrating the method steps for fabricating a magnetic random access memory array in accordance with an embodiment of the present invention.

Figures 5 (a-g) are cross sectional views showing the resulting structures of the flowchart of Figure 5 in accordance with embodiments of the present invention.

Figure 6 is a top perspective view of a structure in accordance with an embodiment of the present invention.

Figure 7 is a computer system in accordance with an embodiment of the present invention.

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Figure 7 is a computer system in accordance with an embodiment of the present invention.